



**C-DAC & Gujarat Technological University M.E.
Electronics & Communication Engineering
(VLSI & Embedded Systems Design)
Gandhinagar**

Semester –III

2735206: Embedded Computing

UNIT – I

Introduction to Embedded Processing, Overview of VLIW and ILP

UNIT – II

Overview of ISA design, Architectural Structures in ISA design, Micro architecture Design,

UNIT – III

System Design and Simulation, Embedded Compiling and Tool chains, Compiling for VLIWs and ILP

UNIT – IV

Runtime System, Application Design and Customization,

UNIT – V

Disciplined Development, Firmware Standards, Sample Drawing System

References:

- 1.Embedded Computing a VLIW Approach to Architecture, Compilers and Tools By Joseph A. Fisher, Elsevier publication
- 2.The Art of Designing Embedded Systems By Jack Ganssle, Elsevier publication